

# SystemC AMS & COSEDA User Group Meeting Program 2022

**Date:** Thursday, November 24th to Friday, November 25th 2022

**Location:** This year's meeting will take place in Dresden (Koenigsbruecker Str. 124 | 01099 Dresden | Germany) as well as an online event

## First Day: Thursday, November 24<sup>th</sup> 2022

### Session 1

- 13:00 – 13:15 **Welcome Speech & COSEDA Technologies Update**  
*Thomas Hartung, COSEDA Technologies GmbH*
- 13:15 – 13:45 **What's new in COSIDE® 3.1 & Forecast for 2023**  
*Karsten Einwich, COSEDA Technologies GmbH*
- 13:45 – 14:15 **Closing the Gap between Requirement Management and System Design using the COSIDE® Jama Integration**  
*Hayri Verner Hasou, Infineon Technologies*
- 14:15 – 14:45 **Virtual Prototyping in SystemC AMS for Validation of Tight Sensor/Firmware Interaction in Smart Sensors**  
*Alexandra Kuester, Bosch Sensortec GmbH*

### Session 2

- 15:15 – 15:35 **Bosch GTM Model Integration**  
*Juergen Hanisch, Robert Bosch GmbH & Karsten Einwich, COSEDA Technologies GmbH*
- 15:35 – 16:05 **Using Triggered SystemC AMS AC Analysis for Run-Time Parameter Extraction**  
*Peter Alfred Friessnegger, Infineon Technologies Austria AG*
- 16:05 – 16:35 **COSIDE® Regressiontestsuite**  
*Thomas Arndt, COSEDA Technologies GmbH*

### Session 3

- 17:00 – 17:30 **RTL to Software Model using COSIDE® and SystemC**  
*Ashot Hambarzumyan, NASA Jet Propulsion Laboratory (California Institute of Technology)*
- 17:30 – 18:00 **Bringing Topology and Technology Variations from Circuit Models into COSIDE® System Models**  
*Sören Kwasigroch, TU Kaiserslautern (Chair of Cyber-Physical Systems)*

**19:00 – 21:00** **User Group Meeting Dinner** at Vault Restaurant Sophienkeller (Taschenbergpalais) in Dresden

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## Second Day: Friday, November 25<sup>th</sup> 2022

### Session 4

- 09:00 – 09:30 **Firmware Bring-Up and Integration on SystemC VP with COSIDE®**  
*Ana-Maria Plaiseanu, Infineon Technologies*
- 09:30 – 10:00 **ARM Performance Model Integration**  
*Thilo Voertler, COSEDA Technologies GmbH*
- 10:00 – 10:30 **COSIDE® as Key Enabler to Introduce IEEE1666.1 SystemC AMS in Education with Focus on Chip Design at FH Kaernten**  
*Wolfgang Scherr, Carinthia University of Applied Sciences (FH Kaernten)*

### Session 5

- 11:00 – 11:30 **Modeling the Automatic Gain Control Loop for DAB Radio**  
*Karl Sturm, NXP Semiconductors*
- 11:30 – 12:00 **Top-Down Radar System Design with COSIDE® - an Overview**  
*Federico Passerini, Robert Bosch AG*
- 12:00 – 13:00 **Wrap-up & Farewell**