

# SystemC AMS & COSIDE® User Group Meeting Program 2016



**Date:** Tuesday - October 18, 2016  
**Location:** Maritim Hotel Munich, Goethestraße 7, 80336 Munich, Germany  
(Located close to the main station)

- 09:00 Registration & Welcome Coffee**
- 09:15 – 09:30 **Welcome Speech & COSEDA Technologies Update**  
*Thomas Hartung, COSEDA Technologies GmbH*
- 09:30 – 09:55 **What`s new in COSIDE® 2.3 & Forecast for 2017**  
*Karsten Einwich, COSEDA Technologies GmbH*
- 09:55 – 10:20 **Design to Test – Closing the Gap to Lab and Production Test**  
*Christian Spiss, National Instruments Corporation*
- 10:20 – 10:50 Coffee Break**
- 10:50 – 11:20 **Tutorial: Bridging the Gap to and from Matlab/Simulink**  
*Paul Ehrlich, COSEDA Technologies GmbH*
- 11:20 – 11:45 **From COSIDE® to Cadence – Mind the Gap**  
*Andreas Schröck, DICE GmbH & Co KG*
- 11:45 – 12:10 **QEMU Integration in COSIDE® and SystemC**  
*Mark Burton, GreenSocs*
- 12:10 – 13:40 Lunch Break & Networking**
- 13:40 – 14:20 **Tutorial: COSIDE® Testbench Concepts and Verification Features**  
*Thilo Vörtler, COSEDA Technologies GmbH*
- 14:20 – 14:45 **System Level Evaluation of UVM-SystemC**  
*Dr. Sacha Loitz, Continental AG*
- 14:45 – 15:10 **Automated Fault Injection for SystemC and SystemC AMS**  
*Stephan Gerth, Fraunhofer IIS/EAS*
- 15:10 – 15:40 Coffee Break**
- 15:40 – 16:10 **Tutorial: AC and AC Noise Simulation with SystemC AMS**  
*Karsten Einwich, COSEDA Technologies GmbH*
- 16:10 – 16:35 **Modeling of an ADC for a RF Transceiver with COSIDE®**  
*Michel Vasilevski, Seamlesswaves*
- 16:35 – 17:00 Demo`s & Wrap-up & Farewell**