



Dear Ladies and Gentlemen,

We are very pleased to invite you to the **SystemC AMS & COSIDE® User Group Meeting 2015**. This year's meeting will take place at the InterCity Hotel Munich, Germany on Tuesday **November 10, 2015**. One day before the start of DVCON Europe 2015.

The electronic system level tool COSIDE® is the first commercially available environment fully supporting the SystemC & SystemC AMS language standards. COSIDE® enables overall system modeling and simulation for analog and digital as well as for hard- and software.

The SystemC AMS & COSIDE® UGM is the event in 2015 for all professionals, which are interested in the latest developments of SystemC AMS and COSIDE®. The meeting provides an open forum for the intensive exchange of knowledge by experienced industrial and academic users.

Participation for User Group Meeting 2015 is free of charge but early registration via Email at Usergroupmeeting@coseda-tech.com is highly recommended.

Program UGM 2015:

Location: InterCity Hotel Munich, Bayerstrasse 10, 80335 München, Germany
(Located directly at the main station)

09:00 Registration & Welcome Coffee

09:15 – 09:30 Welcome Speech & COSEDA Technologies Update

Thomas Hartung, COSEDA Technologies GmbH

09:30 – 09:55 What`s new in COSIDE® 2.1 & Forecast for 2016

Karsten Einwich, COSEDA Technologies GmbH

09:55 – 10:20 Using SystemC Models for pre-silicon development of an ATE Test Suite

Dr. Sacha Loitz, Continental AG

10:20 – 10:50 Coffee Break

10:50 – 11:35 **Tutorial: COSIDE® Export Features**

Thomas Arndt, COSEDA Technologies GmbH

11:35 – 12:00 **A COSIDE® based virtual Radarsensor on the Road**

Florian Starzer, DICE GmbH & Co KG

12:00 – 13:30 Lunch Break & Networking

13:30 – 14:15 **Tutorial: Modelling Nonlinear dynamic Systems with SystemC AMS and COSIDE®**

Karsten Einwich, COSEDA Technologies GmbH

14:15 – 14:40 **Experiment Planning for Simulation-based Verification**

Monica Rafaila, Infineon Technologies AG

14:40 – 15:05 **HiL Firmware Prototyping using an COSIDE® asynchronous Motor Model**

Paul Ehrlich, Fraunhofer IIS/EAS

15:05 – 15:35 Coffee Break

15:35 – 16:00 **Model Based Estimation for Mixed Signal System Optimization**

Sumit Adhikari, NXP Semiconductors

16:00 – 16:45 **Tutorial: UVM SystemC in COSIDE®**

Stephan Schulz, Fraunhofer IIS/EAS

16:45 – 17:00 Wrap-up & Farewell

If you require any further information please do not hesitate to contact us at thomas.hartung@cosedatech.com.

We look forward to welcoming you.

With best regards,

Thomas Hartung
Marketing

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