

# Formal Verification of UML Statechart Diagrams with COSIDE®

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#### **Formal Verification**



Def.: Includes all mathematical techniques to verify security and/or correctness of software or hardware.

- Possible techniques:
  - refinement,
  - theorem proving,
  - model checking,
  - equivalence checking
- Possible application fields:
  - security protocol verification,
  - software verification,
  - hardware verification



### Why Formal Verification

- Security Products:
  - eGovernment (e.g. electronic Passport)
  - Bank Cards (e.g. Credit Cards)
  - Smart Mobility & Access Management Cards







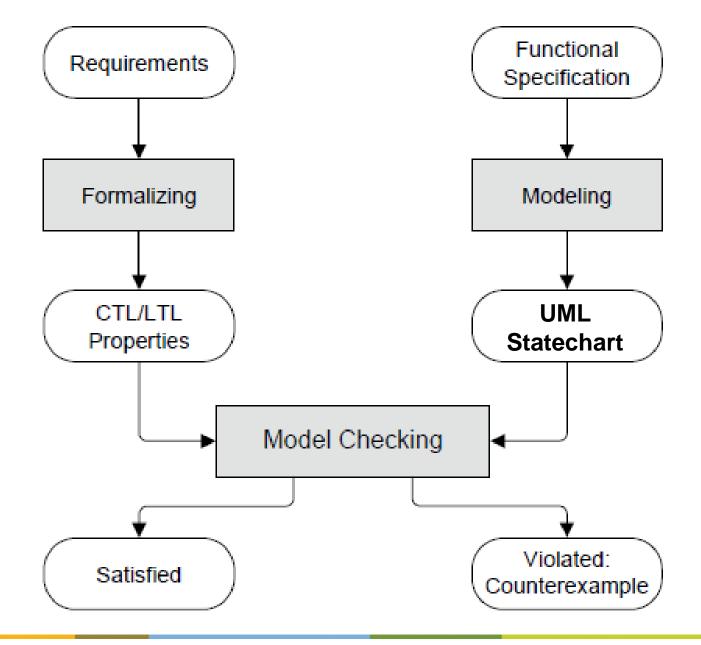


## Why Formal Verification of UML Statechart Diagrams

- Mathematical proof that the functional specification satisfies the requirements
  - Requirements what
  - Specification how
- Feasible but still useful:
  - Find errors early before they are implemented
  - Generate precise/unambiguous understanding of the specification
  - Increases assurance as required for e.g. Common Criteria certification



#### How

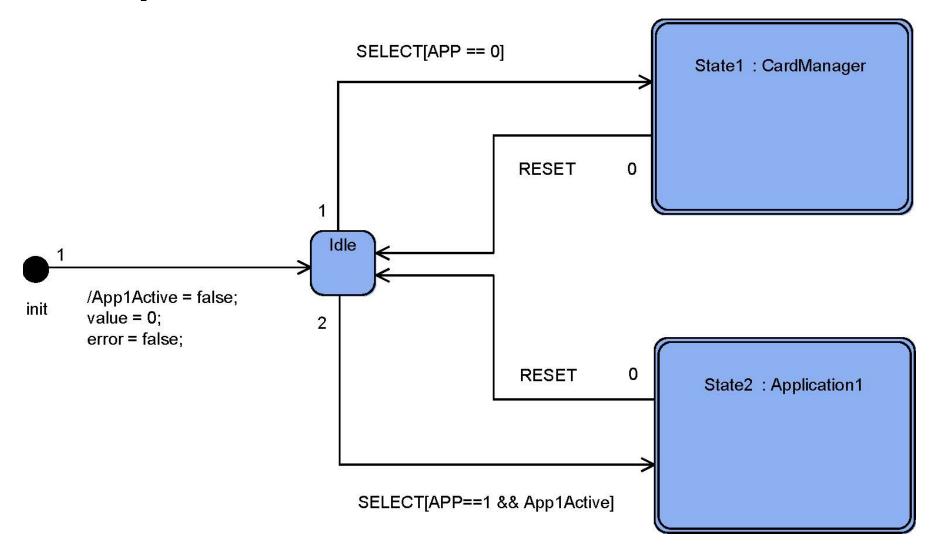




### Simplified Example – Access Control Policy

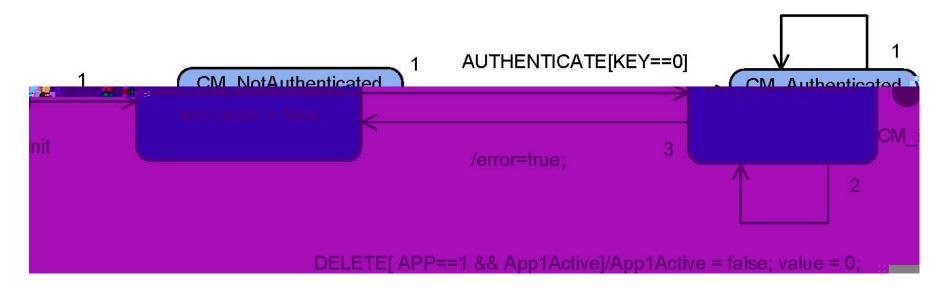
- 2 Features
- A public transport company can create/delete an application on the card (has to be authenticated with KEY = 0).
- A customer can incremented and decremented the value stored in the application (has to be authenticated with KEY = 1).
- Modeled with COSIDE®



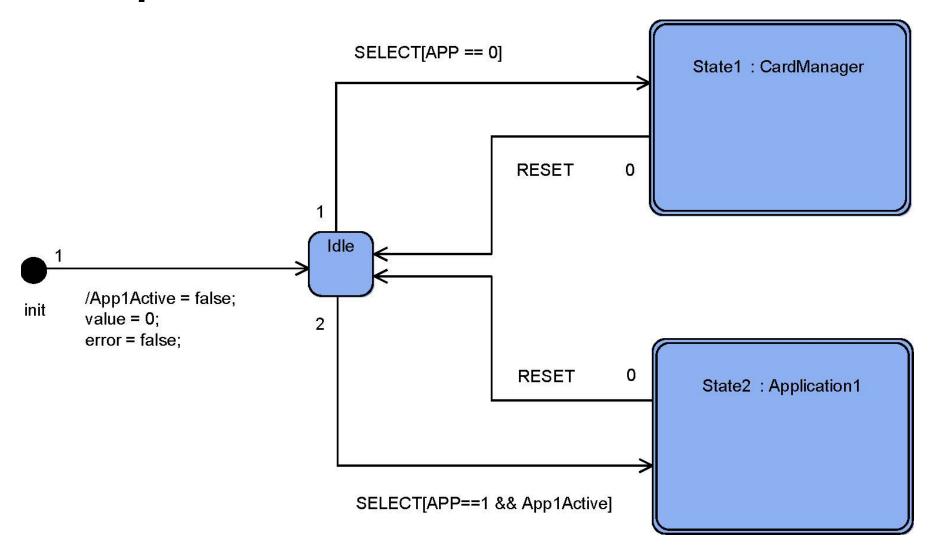




CREATE[ APP==1 && !App1Active]/App1Active = true; value = 0;

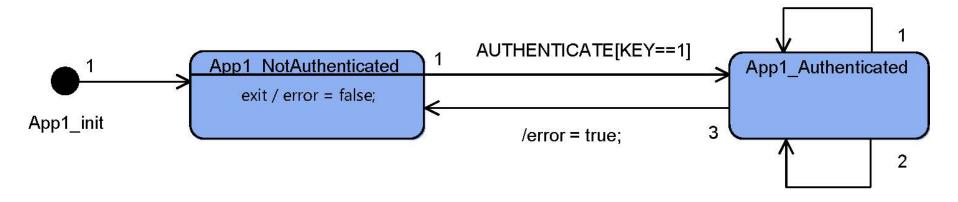








INCREMENT[value<5]/value = value+1;</pre>



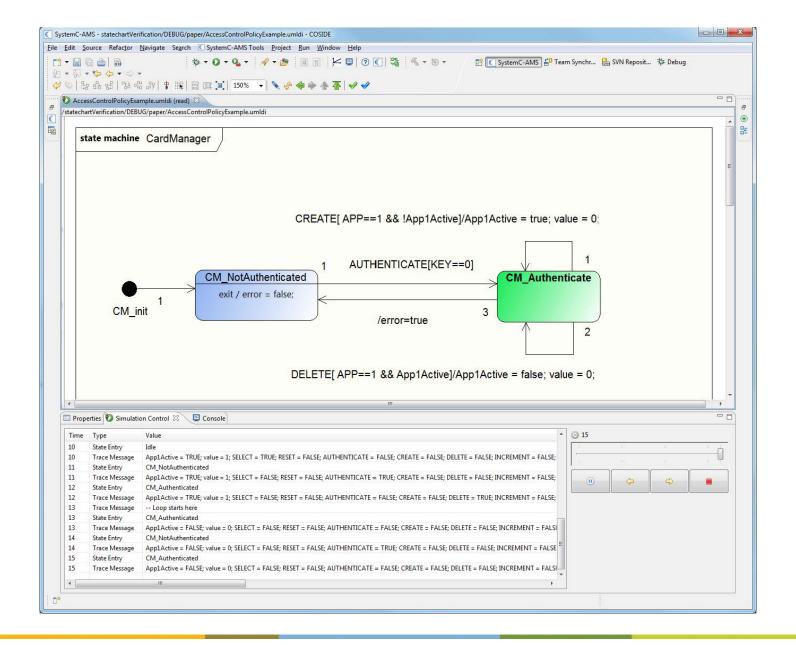
DECREMENT[value>0]/value = value-1;



It is only possible to create an application when authenticated with the card manager key.

- Model checker proves the property.
- For properties that can not be proven a counter example is given.
- Counter examples can be visualized.







#### **Summary**

- We formally prove that the functional specification (UML state diagram) satisfies the requirements (temporal logic formula).
- Modeling the specification and using an input language that is understood by engineers, helps to
  - avoid errors at the specification phase
  - generate a common understanding of the specification
- Ensure high quality and security enabling certification

