

COSIDE® by COSEDA Technologies

The Design Platform for Faster & Safer System Development



System Level Environment

With **COSIDE®**, the innovative design platform from COSEDA Technologies, you can **simplify complex development processes** – from **modeling and simulation to traceability, integration, and verification**.

As the **only commercial design environment** supporting both **SystemC** and **SystemC AMS** (IEEE 1666-2011 / IEEE 1666.1-2016), **COSIDE®** empowers you to develop **cutting-edge analog and digital systems** with maximum efficiency and reliability.

Overall System Modeling

COSIDE® bridges the gap between analog and digital, hardware and software.

Its **holistic design approach** unifies all development domains, enabling **seamless workflows** across your entire project.

With **COSIDE®**, you unlock the full potential of **system-level modeling**,

hardware/software co-design, verification, and virtual prototyping – ensuring faster innovation and reduced development risks.

COSIDE® enables:

▪ Modeling & Design

Accelerate development with **virtual prototypes** built from a **library of 1.000+ elements**, covering RF, communication, automotive, power electronics, and mechanical domains.

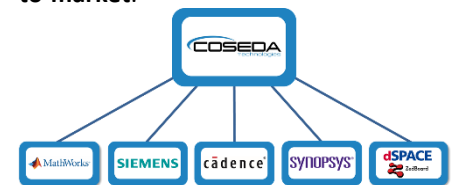
Design **abstract models** that are **fast, accurate, reusable, and license-free to run**.

Your **system-level model becomes the golden reference**, providing an **automated, synchronized path from algorithm to concept, system, and final implementation** – ensuring consistency and quality at every step.

▪ Design Sharing

Break down barriers in collaboration: with **COSIDE®**, your designs can be **shared seamlessly across EDA tools, teams, and even customers**.

Thanks to flexible **import/export capabilities** and **multiple encryption levels**, you benefit from **efficient workflows, secure data exchange, and faster time-to-market**.



▪ System Level Verification

Ensure reliability from day one: with **COSIDE®**, you create a **unified test environment** for hardware and software models – including **fault injection and regression testing**.

This environment can be **reused across all design levels down to the lab**, safeguarding consistency and reducing effort.

Leverage **advanced digital and analog mixed-signal regression and check features**, seamlessly deployable into your **continuous integration workflow** – for **higher productivity, faster validation, and robust designs**.

Main Features of COSIDE®

Extensive Modeling Capabilities

- 1,000+ ready-to-use library elements (RF, automotive, power electronics, mechanics, etc.)
- Full support of SystemC & SystemC AMS (IEEE 1666-2011 / IEEE 1666.1-2016)
- Analog & mixed-signal hardware/software models
- Transaction Level Modeling (TLM)

Integration & Interoperability

- Seamless integration with leading EDA tools (Siemens, Matlab, Cadence, Synopsys, etc.)
- Spice integration and virtual platforms (ARM, Intel, and more)
- IP-XACT support
- Easy Excel import/export
- Flexible model import/export with IP protection

Verification & Quality Assurance

- Advanced analog & digital checker capabilities
- Block-level and formal verification
- UVM & assertion-based mixed-signal verification
- System-level Monte Carlo simulation
- Continuous integration support for all verification and testing tasks

Productivity & Cost Efficiency

- License-free simulations on simple PC setups
- Automated code generation & integrated version control
- Easy-to-use schematic design entry
- Scriptable mixed-signal wave viewer
- Support for ISO 26262 compliant design processes

Platform Support

- Supported operating systems: Windows & Linux
- Virtual platforms covering all major processor models (ARM, Intel, etc.)

Motivation to use COSIDE®

Save time, cut costs, and boost quality – these are the key benefits our users experience with COSIDE®. In combination with SystemC & SystemC AMS, COSIDE® provides a powerful approach to mastering even the most complex design challenges.

Faster Time-to-Market

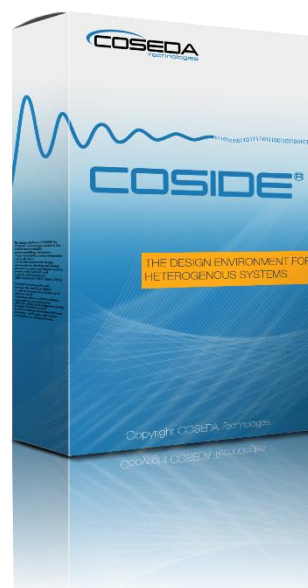
- Parallel hardware & software development accelerates your workflow
- Reduced simulation time speeds up iterations
- Reusable test environments ensure efficiency across all design levels

Cost Reduction

- License-free simulations save significant expenses
- Simple PC setup replaces costly hardware investments
- Shorter development cycles cut overall project costs

Improved Quality

- End-to-end verification of analog, digital, and software components
- Continuous integration & regression testing for reliable results
- Reproducible outcomes from concept phase to lab ensure consistency



With COSEDA to System Level Design

At **COSEDA Technologies**, **customer proximity is our strength**. Our experienced engineering team focuses on your **individual needs** and responds with **maximum flexibility**.

We accompany you throughout the **entire COSIDE® implementation process** and **provide tailored services and training** – empowering your designers with the **latest solutions** and preparing them for the future of **system-level design**.

Conception and Implementation

The **COSEDA Technologies team** showcases the full **performance and feasibility of COSIDE®** during evaluation. To ensure a smooth transition, we provide **comprehensive technical support** throughout the transfer process – guaranteeing an **efficient and successful implementation** of COSIDE® in your workflow.

Support Service:

We offer **tailored, customer-specific services** designed to meet your unique requirements.

With our dedicated **technical support**, you gain a reliable partner to **accelerate future developments, reduce risks, and ensure long-term success** with COSIDE®.

Trainings and Workshops:

Boost your team's expertise with our **custom trainings and interactive workshops**.

We prepare your designers for **future challenges in system-level design**, equipping them with the **skills, methods, and confidence** to master tomorrow's projects.

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