

SystemC AMS & COSEDA User Group Meeting Program 2019



Date: Monday - October 28, 2019
Location: Maritim Hotel Munich, Goethestrasse 7, 80336 Munich, Germany
(Located at the main station)

- 09:30 Registration & Welcome Coffee**
- 09:45 – 10:00 **Welcome Speech & COSEDA Technologies Update**
Thomas Hartung, COSEDA Technologies GmbH
- 10:00 – 10:30 **What`s new in COSIDE® 2.6 & Forecast for 2020**
Karsten Einwich, COSEDA Technologies GmbH
- 10:30 – 10:55 **Digital on Top - Verilog Co-simulation**
Thomas Arndt, COSEDA Technologies GmbH
- 10:55 – 11:30 Coffee Break**
- 11:30 – 11:55 **High Level Register Modelling Approach within an LiDAR ASIC with COSIDE®**
Klemens Kordik, LeddarTech Inc.
- 11:55 – 12:20 **COSIDE® supported Multi Simulator SystemC usage – from Customer till Design**
Peter Friessnegger, Infineon Technologies AG
- 12:20 – 12:45 **SystemC AMS as Key Component to DLR Spacecraft Power System Design Automation**
Janis Haesecker, German Aerospace Center (DLR), Institute of Space Systems
- 12:45 – 14:10 Lunch Break & Networking**
- 14:10 – 14:35 **Constraint Randomization with CRAVE**
Muhammad Hassan, German Research Center for Artificial Intelligence (DFKI)
- 14:35 – 15:00 **UVM-SystemC – Functional Coverage & Constrained Randomization**
Stephan Gerth, Bosch Sensortec GmbH
- 15:00 – 15:25 **COSIDE® UVM Integration**
Thilo Voertler, COSEDA Technologies GmbH
- 15:25 – 15:50 **Directed Testing using UVM-SystemC**
Sacha Loitz, Continental Teves AG & Co oHG
- 15:50 – 16:15 Coffee Break**
- 16:15 – 16:40 **Extending a SysML based Platform Model Generation Flow to support AMS**
Ralph Goergen, NXP Semiconductors
- 16:40 – 17:05 **Accelerating Analog Design with SystemC AMS**
Américo Dias, Independent Consultant
- 17:05 – 17:30 **COSIDE® Postprocessing – FFT & Co**
Paul Ehrlich, COSEDA Technologies GmbH
- 17:30 – 18:00 Wrap-up & Farewell**