

SystemC AMS & COSIDE® User Group Meeting Program 2017



Date: Thursday - October 19, 2017
Location: InterCity Hotel Munich, Bayerstrasse 10, 80335 Munich, Germany
(Located at the main station)

- 09:00 Registration & Welcome Coffee**
- 09:15 – 09:30 **Welcome Speech & COSEDA Technologies Update**
Thomas Hartung, COSEDA Technologies GmbH
- 09:30 – 10:00 **What`s new in COSIDE® 2.4 & Forecast for 2018**
Karsten Einwich, COSEDA Technologies GmbH
- 10:00 – 10:25 **Product Modeling @ SC Villach: Using SystemC and COSIDE® to enable effective IC development and customer support**
Simone Fontanesi, Infineon Technologies
- 10:25 – 10:55 Coffee Break**
- 10:55 – 11:20 **The World's First Real-Time Millimeter Wave Radar Simulator using 3D CG MAP/Objects and Use-case**
Takahiro Yanagi, OSTL Japan & Hiroshi Naganawa, DENSO International America Inc.
- 11:20 – 11:45 **AC and Noise Simulation for an Ethernet Phy**
Gerhard Nössing, Intel Corporation
- 11:45 – 12:10 **Tutorial: COSIDE® Piece Wise Linear Modelling**
Karsten Einwich, COSEDA Technologies GmbH
- 12:10 – 13:40 Lunch Break & Networking**
- 13:40 – 14:20 **Tutorial: Analog Checker Library**
Thilo Vörtler, COSEDA Technologies GmbH
- 14:20 – 14:45 **Reusing SystemC models in heterogeneous environments by COSIDE® coupling and export**
Dr. Sacha Loitz, Continental AG
- 14:45 – 15:10 **COSIDE® Synopsys® Bridge – Advancing architecture exploration**
Jan Kastning, NXP Semiconductor
- 15:10 – 15:40 Coffee Break**
- 15:40 – 16:05 **Analog IP – With Intelligent IP from system to silicon**
Torsten Reich, Fraunhofer IIS/EAS
- 16:05 – 16:30 **Tips & Tricks – Getting more productive with COSIDE®**
Thomas Arndt, COSEDA Technologies GmbH
- 16:30 – 17:00 Demo's & Wrap-up & Farewell**