

# SystemC AMS Day 2011

**Date:** May 12, 2011

**Location:** Hotel Koenigshof, Dresden, Germany

- 8.00 – 8.30      **Registration**
- 8.30 – 8.40      **Welcome, Agenda & Introduction**  
Martin Barnasconi, NXP Semiconductors, The Netherlands  
Karsten Einwich, Fraunhofer IIS/EAS, Germany
- 8.40 – 9.00      **OSCI Update**  
Martin Barnasconi, NXP Semiconductors, The Netherlands  
OSCI AMS Working Group Chair

## **BLOCK 1:      SYSTEMC AMS FOR SYSTEM INTEGRATORS**

- 9.00 – 9.30      **Modelling and Simulation of a Fibre Optical Gyro System with SystemC AMS**  
Oliver Waydhas, Northrop Grumman LITEF GmbH, Germany
- 9.30 – 10.00      **SystemC AMS-based Virtual Platform for Automotive Electronic Systems  
Development & Verification**  
Ingmar Neumann, Continental Corporation, Germany
- 10.00 – 10.30      **Automatic Transformation of MATLAB/Simulink Models to SystemC AMS**  
Nico Bannow, Robert Bosch GmbH, Germany  
Ralph Görden, OFFIS Institute, Germany  
Wolfgang Nebel, University of Oldenburg, Germany
- 10.30 – 11.00      *Coffee, networking, poster sessions, demonstrations and show-cases*

## **BLOCK 2:      SYSTEMC AMS FOR AUTOMOTIVE AND SENSORS SEMICONDUCTOR INDUSTRY**

- 11.00 – 11.30      **An Efficient Transceiver Design Verification Method by Means of SystemC AMS  
– VHDL Co-simulation**  
Gerhard Deutsch, Infineon Technologies, Austria
- 11.30 – 12.00      **SystemC AMS Model of a CMOS Video Sensor**  
Fabio Cenni, Serge Scotti, STMicroelectronics, France  
Emmanuel Simeu, TIMA Laboratory, France
- 12.00 – 12.30      **SystemC Executable Specification of a Magnetic Speed Sensor**  
Tobias Werth, Infineon Technologies, Austria
- 12.30 – 13.30      *Lunch, networking, poster sessions, demonstrations and show-cases*

## **BLOCK 3:      SYSTEMC AMS DESIGN METHODOLOGIES, EDA TOOLS AND FLOWS**

- 13.30 – 14.00      **Introducing Analog Parts into TLM Virtual Platforms**  
Yossi Veller, Mentor Graphics, Israel
- 14.00 – 14.30      **Using IEEE 1685 Standard (IP-XACT) for Managing AMS Design Flow Based on  
SystemC AMS**  
Emmanuel Vaumorin, Magillem Design Services, France

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<b>BLOCK 4:</b>	<b>SYSTEMC AMS IN WIRELESS AND WIRED COMMUNICATION SEMICONDUCTOR INDUSTRY</b>
14.30 – 15.00	<b>SystemC/-AMS System-level Model of a Near Field Communication (NFC) Radio Front-end</b> Bas Arts, NXP Semiconductors, The Netherlands
15.00 – 15.30	<b>SystemC AMS Modelling of a Metallic Line Testing System</b> Gerhard Nössing, Lantiq, Austria
15.30 – 16.00	<i>Coffee, networking, poster sessions, demonstrations and show-cases</i>
<b>BLOCK 5:</b>	<b>RESEARCH AND ACADEMIC</b>
16.00 – 16.30	<b>A Range-based System Simulation and Refinement Design Flow</b> Florian Schupfer, Markus Svarc, Carna Radojicic, Christoph Grimm, Vienna University of Technology, Austria
16.30 – 17.00	<b>A Monolithic 3-Phase Grid-Tie Direct Current (DC) Alternating Current (AC) Inverter</b> Amal Banerjee, Balmiki Sur, Jim Freeman and Andreas Gerstlauer, University of Texas, Austin, USA
<b>BLOCK 6:</b>	<b>INTERACTIVE FORUM</b>
17.00 – 17.30	<b>SystemC AMS Standard and Proof-of-Concept: Status and Outlook</b> Karsten Einwich, Fraunhofer IIS/EAS, Germany
17.30 – 18.00	<b>Interactive Discussion: OSCI SystemC AMS Standardization – Next Steps Versus Your Requirements &amp; Needs</b> Moderator: Christoph Grimm, Vienna University of Technology, Austria Panel: Karsten Einwich, Fraunhofer IIS/EAS, Germany Serge Scotti, STMicroelectronics, France Wolfgang Scherr, Infineon Technologies, Austria Martin Barnasconi, NXP Semiconductors, The Netherlands
18.00	<b>Wrap-up and Closure</b>